

PATENT

FO660

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Date: 10-9-02
Himanshu S. Amin

#3
Election
J. Main/AB
10/10/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Applicants: Bharath Rangarajan, *et al.*

Serial No: 09/893,803

Filing Date: June 28, 2001

Examiner: Stanetta D. Isaac

Art Unit: 2812

Title: SYSTEM AND METHOD FOR CREATION OF SEMICONDUCTOR MULTI-SLOPED FEATURES

Assistant Commissioner for Patents
U.S. Patent and Trademark Office
Washington, D.C. 20231**FAX COPY RECEIVED**

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REPLY TO RESTRICTION REQUIREMENT MAILED SEPTEMBER 23, 2002

This Reply is in response to the Restriction Requirement mailed on September 23, 2002 in connection with the above-identified patent application.

The Examiner requires restriction to one of the following two groups of claims:

- I. Claims 1-7, and 13, drawn to apparatus, classified in class 356, subclass 1+.
- II. Claims 8-12, drawn to a method, classified in class 438, subclass 8.

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Applicants' representative hereby elects with traverse Group I (claims 1-7, and 13, drawn to apparatus, classified in class 356, subclass 1+) for further prosecution on the merits.

Should there be any questions regarding this paper, the Examiner is invited to contact applicants' undersigned representative at the telephone number listed below.

In the event any fees are due in connection with submission of this document, the Commissioner is authorized to charge such fees to Deposit Account No. 50-1063.

Respectfully submitted,
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